

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of
Invention

METHOD OF MAKING A finFET HAVING SUPPRESSED
PARASITIC DEVICE CHARACTERISTICS

Application Number:

Confirmation Number:

First Named Applicant: Edward Nowak

Attorney Docket Number: BUR920030005US1

Search string: (6475869 or 6413802 or 6406962 or 6297531 or 6215156 or 6214670 or
5899710 or 5804848 or 5793082 or 5773331 or 5646058 or 5604368 or
5356824 or 5122848 or 5119153 or 20020171107 or 20020008280).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
<input checked="" type="checkbox"/>	1	6475869	2002-11-05	Yu		438	303
<input type="checkbox"/>	2	6413802	2002-07-02	Hu et al.		438	151
<input type="checkbox"/>	3	6406962	2002-06-18	Agnello et al		438	268
<input type="checkbox"/>	4	6297531	2001-10-02	Armacost et al.		257	329
<input type="checkbox"/>	5	6215156	2001-04-10	Yang		257	360
<input type="checkbox"/>	6	6214670	2001-04-10	Shih et al.		438	259
<input type="checkbox"/>	7	5899710	1999-05-04	Mukai		438	156
<input type="checkbox"/>	8	5804848	1998-09-08	Mukai		257	270
<input type="checkbox"/>	9	5793082	1998-08-11	Bryant		257	330
<input type="checkbox"/>	10	5773331	1998-06-30	Solomon et al.		438	164
<input type="checkbox"/>	11	5646058	1997-07-08	Taur et al.		437	40
<input type="checkbox"/>	12	5604368	1997-02-18	Taur et al.		257	348
<input type="checkbox"/>	13	5356824	1994-10-18	Chouan et al.		437	41
<input type="checkbox"/>	14	5122848	1992-06-16	Lee et al.		357	23.6
<input checked="" type="checkbox"/>	15	5119153	1992-06-02	Korman et al.		357	23.4

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

Init	Cite.No.	Pub. No.	Date	Applicant	Kind	Class	Subclass
HA	1	20020171107	2002-11-21	Cheng et al.		257	347
HA	2	20020008280	2002-01-24	Armacost et al.		257	330

Signature

Examiner Name	Date
<i>Wang L. Chen</i>	<i>11/6/04</i>



FORM PTO-1449 (Modified)

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S SUPPLEMENTAL INFORMATION
DISCLOSURE STATEMENT

Page 1 of 1

(Use several sheets if necessary.)

ATTY DOCKET NO.
BUR920030005US1SERIAL NO.
10/604,086APPLICANT:
Nowak et al.FILING DATE:
June 25, 2003GROUP:
Not yet assigned

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINERS INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (IF APPRO.)
	AA						
	AB						
	AC						

U.S. PATENT APPLICATION PUBLICATIONS

	AD						
	AE						
	AF						
	AG						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
oko	AH	EP0763851	19.03.1997	Europe				

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

oko	AI	<i>A Bulk-Si-compatible Ultrathin-body SOI Technology for sub-100nm MOSFETs</i> ; V. Subramanian, J. Kedzierski, N. Lindert, H. Tam, Y. Su, J. McHale, K. Cao, T-J. King, J. Bokor, & C. Hu; Department of Electrical Engineering and Computer Sciences, University of California, Berkeley, CA; date unknown.
oko	AJ	<i>A Folded-channel MOSFET for Deep-sub-tenth Micron Era</i> ; Digh Hisamoto, wen-Chin Lee, Jakub Kedzierski, Erik Anderson, Hideki Takeuchi, Kazuya Asano, Tsu-Jae King, Jeffrey Bokor, and Chenming Hu; Central Research Laboratory, Hitachi Ltd., EECS, UC Berkley, Lawrence Berkeley Laboratory, Nippon Steel Corp, NKK Corp.; 0-7803-4777-3/98 - 1998 IEEE
oko	AK	<i>Sub 50-nm FinFET: PMOS</i> ; Xuejue Huang, Wen-Chin Lee, Charles Kuo, Digh Hisamoto, Leland Chang, Jakub Kedzierski, Erik Anderson, Hideki Takeuchi, Yang-Kyu Choi, Kazuya Asano, Vivek Subramanian, Tsu-Jae King, Jeffrey Bokor and Chanming Hu; Department of Electrical Engineering and Computer Sciences, University of California at Berkeley, CA; 0-7803-5413-3/99; 1999 IEEE
oko	AL	<i>A Fully Depleted Lean-Channel Transistor (DELTA) - A Novel Vertical Ultrathin SOI MOSFET</i> ; Digh Hisamoto, Toru Kaga, Yoshifumi Kawamoto and Edi Takeda; IEEE Electron Device Letters, Vol. 11, No. 1, January 1990; 0741-3106/90/0100-0036\$01.00.

EXAMINER

DATE CONSIDERED

7/10/04

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.